

FIG.1

Processor Memory

251

251

TERMINAL

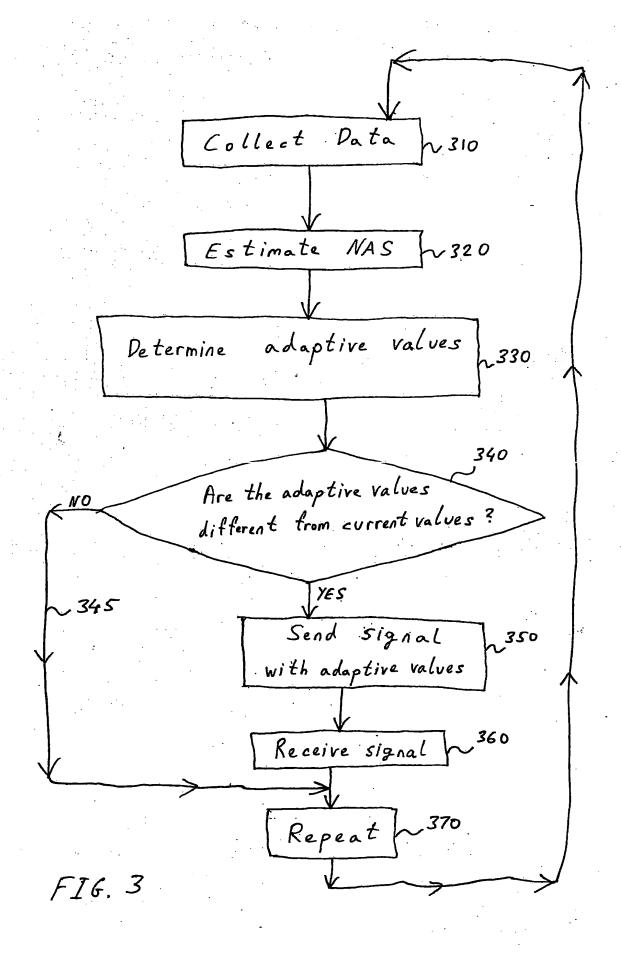
STATION

STATION

1 STATION

1 220

FIG. 2



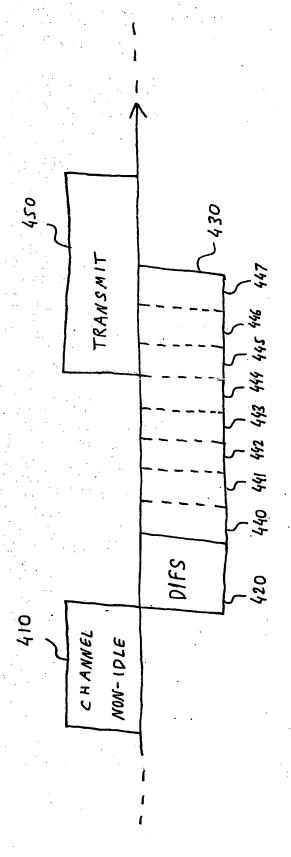


FIG. 4